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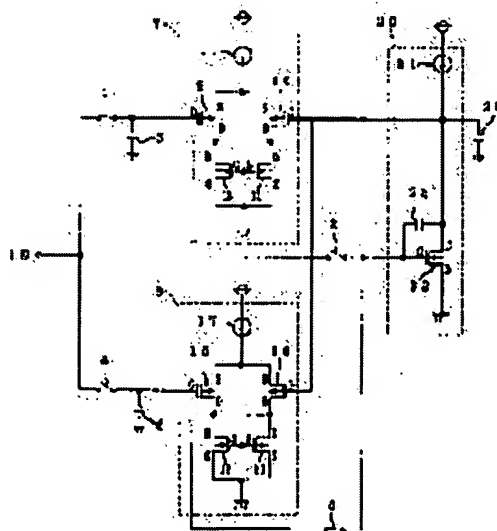
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(54) SAMPLING AND HOLDING CIRCUIT

(57)Abstract:

PROBLEM TO BE SOLVED: To provide a sampling and holding circuit capable of reducing the number of constitutional elements.

SOLUTION: Switches 1, 4 and switches 2, 3 are alternately turned on/off at prescribed timing, and when the switch 1 e.g. is turned on, the switch 2 is turned off and the voltage of a picture signal inputted to an input terminal IN is held in a holding capacitor 5. During the period, the switch 3 is OFF and the switch 4 is ON, the voltage of a picture signal held in a holding capacitor 6 is outputted to an output buffer part 20 and a liquid crystal cell 25 is driven by the voltage.



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